<u>Remarks</u>

Claims 1-5 are presently pending and stand rejected. Claims 6-11 are cancelled without prejudice.

Examiner has indicated that claims 1 and 4 are rejected under 35 U.S.C. § 101 "because 'first circuitry' and 'second circuitry' are just software per se that resides in a memory (See: Specification Page 8 lines 10-18 and [0026])." Assignee respectfully disagrees. Although the cited portion indicates that "<u>An</u> embodiment of the present invention <u>can</u> be implemented as a file resident in the random access memory 64 of one or more computer systems 58 configured generally as described in FIG. 3", the specification does not indicate that the claimed "first circuitry" and the claimed "second circuitry" are just software <u>per se</u>. For example, Examiner's attention is also called to paragraph 0023, which indicates:

> "The hardware emulator 200 comprises a sea of logic and other circuitry 205. The sea of logic and other circuitry 205 is configurable to realize a vast number of integrated circuits. The sea of logic and other circuitry can be divided into a plurality of portions 210. One portion 210(1) can be configured to realize a first SOC, SOC1. The second portion 210(2) can be configured to realize a second SOC, SOC2."

Specification, [0023].

It is therefore submitted that in the claimed "hardware emulator further comprising: a first circuitry ... and a second circuitry", in view of the <u>complete</u> specification, the first circuitry and second circuitry are not just software per se. Accordingly, Examiner is respectfully requested to withdraw the rejection to claims 1-5 under 35 U.S.C. § 101.

Claim 1 was rejected under 35 U.S.C. § 102(b) as being anticipated by Hill. Claim 1 recites, among other limitations "hardware emulator further

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comprising: a first circuitry ... and a second circuitry". Examiner has indicated that Hill teaches "a first circuitry (such as emulator of hardware Model B; See: Fig. 1) . and a second circuitry (such as emulator of Hardware Model C; See: Fig. 1)".
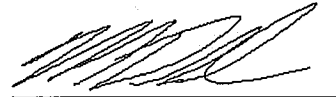
It is respectfully submitted that "emulator of Hardware Model B" and "emulator of Hardware Model C" do not teach "a hardware emulator ... further comprising: a first circuitry ... and a second circuitry". Accordingly, Examiner is requested to withdraw the rejection to claim 1 and dependent claims 2 and 3.

Additionally, Examiner is requested to withdraw the rejection to claims 4

The Commissioner is hereby authorized to charge any additional fees or credit any overpayment to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

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Respectfully submitted,

Mirut Dalal
ATTORNEY FOR ASSIGNEE
Reg. No. 44,052

McAndrews, Held & Malloy, Ltd.
500 West Madison – Suite 3400
Chicago IL 60661

Phone (312) 775-8000
FAX   (312) 775-8100